

11017 U.S. PTO  
10/07/2002  
10/07/2002

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10071298	FILING DATE 02/07/2002	CLASS 361	SUBCLASS 58	GAU 2836	EXAMINER BENENSON
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\*\*APPLICANTS: Belgacem Haba;

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\*\*CONTINUING DATA VERIFIED:  
THIS APPLICATION IS A CIP OF 09/564,064 05/03/2000

BA

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	9797-0074-999
TITLE : Semiconductor module with serial bus connection to multiple dies U.S. DEPT. OF COMM/PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED:		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
TERMINAL		Sheets Drwg.	Figs. Drwg.	Print Fig.
DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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